PRADEEP PAL T

Arulappan st,Manjampatty,Manapparai,Trichy 621307

suryapraveen657@gmail.com
8056557163
DOB 03/04/2002

Objective

To continue my career with an organization that will utilize my MANAGEMENT, SUPERVISION & ADMINISTRATIVE skills to benefit mutual growth and success.

Experience

Amphenol Omniconnect   19.08.2022 - 19.09.2022
Quality checking
I do an internship for one month in the field of fiber cable production company

Education

Anna university
BE-Electronics and Communication Engineering
2023 — **7.01**

Thiyagesar alai Hr sec school
HSC
2019 — **63.33%**

St,Antony's Hr Sec School
SSLC
2017 — **84%**

Skills

- Quick learner
- Problem handling
- Good communication

Projects

Smart farming using IOT
The objective of this project is to offer assistance to farmers in getting Live Data (Temperature, Humidity, Soil Moisture, Soil Temperature) for efficient environment monitoring which will enable them to increase their overall yield and quality of products. This smart agriculture using IoT system powered by NodeMCU consists of a DHT11 sensor, Moisture sensor, DS18B20 Sensor Probe, LDR, Water Pump, and 12V led strip. When the IoT-based agriculture monitoring system starts, it checks the Soil moisture, temperature, humidity, and soil temperature.

Designing FIR filter using Adders and Multipliers
A comparative study of Adders, multipliers and different combination of FIR Filters taken from different sources. FIR Filter designed by using different block which are Adders, Multipliers, and Delay elements. Digital multipliers are mostly used in designing new gadgets, and have many other applications in digital signal processing. FIR Filters are easy to design and are less power consuming. [1] This paper consist of several adders and multiplier like Ripple carry adder, Carry Look Ahead Adder, Han Carlson Adder, Dadda Multiplier, Booth Multiplier, and Vedic Multiplier etc. [2] The focus in this paper is on the parameter of adders, Multiplier and Fir Filters such as Power, LUT utilization, area and delay to find out an efficient FIR Filters with Vedic Mathematics concept. Among these multipliers Vedic Multiplier is the high speed and low power consuming multiplier.

Language

Tamil, English

Achievements & Awards

Designing Fir filter using Adders and Multipliers

Signature: